NUC126 USBD MSD Firmware Update

Example Code Introduction for 32-bit NuMicro® Family

Information

Application	The MassStorage ISP supports to download firmware to APROM through mass storage interface. There is no any tool or driver installation required before downloading firmware.
BSP Version	NUC126 Series BSP CMSIS V3.00.003
Hardware	NuTiny-SDK-NUC126VG4AE_V1.0

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1 Function Description

1.1 Introduction

Mass Storage ISP is one In-System Programming methodology to support download firmware to APROM of NuMicro® Family through mass-storage interface. By using mass-storage interface, we don't need to provide any tool or driver for PC to support firmware download. For a device which supports Mass Storage ISP, user just needs to plug-in the device by USB cable. Then PC will recognize it as a mass-storage disk and user just needs to "COPY" a binary firmware image to the mass-storage disk to do "firmware download".

1.2 Demo Result

On Power-On, the Mass Storage ISP will enter Application Upgrade mode if the specified GPIO pin is low. Then, PC can detect a mass storage class device when the NuMicro® MCU is plugged into PC USB port.

Then, we drag or copy-and-paste the binary file of upgraded application code into window of the mass storage device.

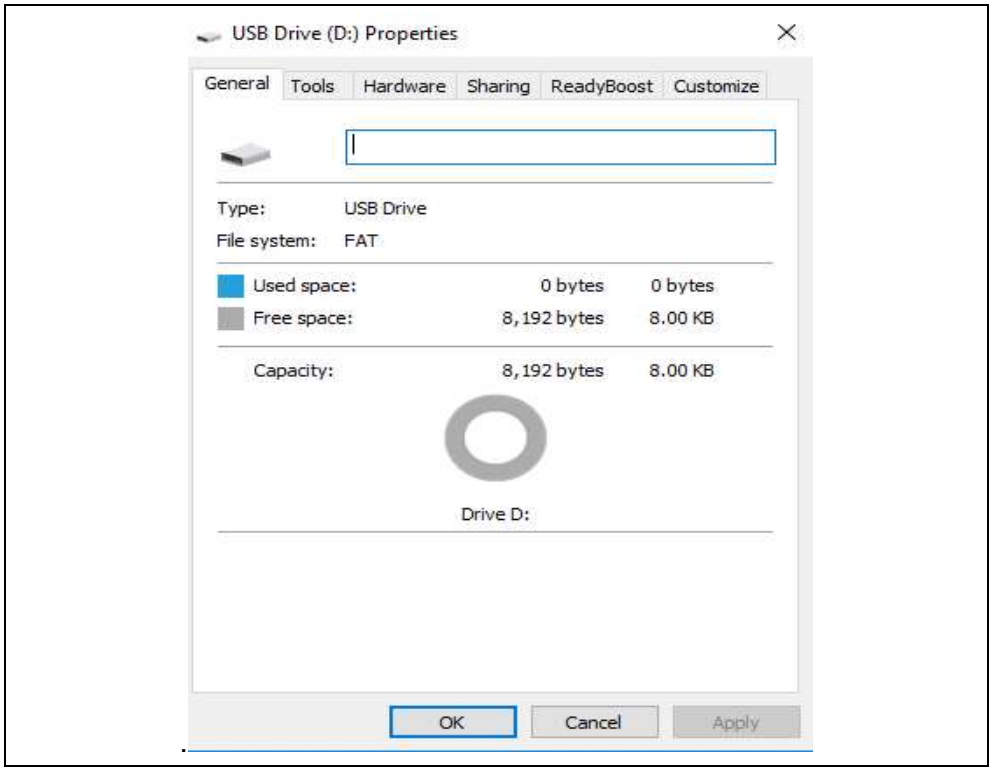


Figure 1 Device Tree

2 Code Description

2.1 Scatter Set

The bin file size is over LDROM(4k), so this sample code configure bit must setting boot in "**LDROM WITH IAP**", the scatter set LDROM 4k and share APROM 4k.

```
LR_IROM1 0x00100000 0x00001000  {    ; load region size_region
  ER_IROM1 0x00100000 0x00001000  {  ; load address = execution address
   *.o (RESET, +First)
   *(InRoot$$Sections)
   .ANY (+RO)
  }
  RW_IRAM1 0x20000000 0x00005000  {  ; RW data
   .ANY (+RW +ZI)
  }
}
```

→ LDROM 4k

```
LR_IROM2 0x0003e000 0x00001000  {
  ER_IROM2 0x0003e000 0x00001000  {  ; load address = execution address
   .ANY (+RO)
  }
}
```

→ Share APROM 4k

2.2 Mass Store ISP Booting Flow

The Mass Storage ISP program will enter application upgrade mode or reset to boot from APROM according to the status of the specified GPIO pin. In the sample code, GPB15 is used to control Mass Storage ISP to reset to APROM or do mass-storage (application upgrade mode).

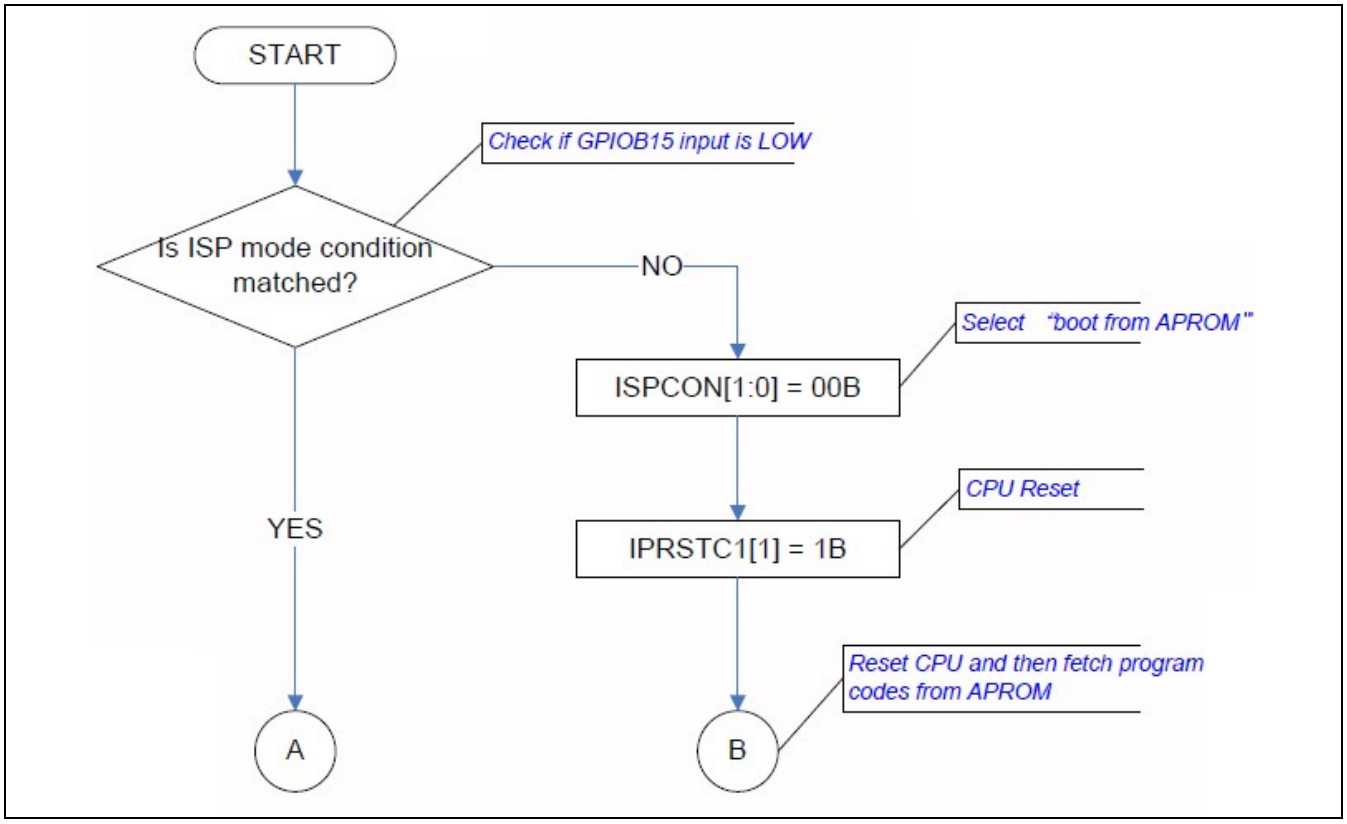


Figure 2: Boot Flow

2.3 Mass Storage ISP Main Flow

When entering application upgrade mode, the Mass Storage ISP program will check APROM size, update FAT table and configure NuMicro® MCU as mass storage class device. After all initial steps have been done successfully; it is ready to upgrade new application code transferred from Host.

After upgrade operation has been completed, User needs to set GPB15 as high and reset the NuMicro® MCU to execute the new firmware in APROM.

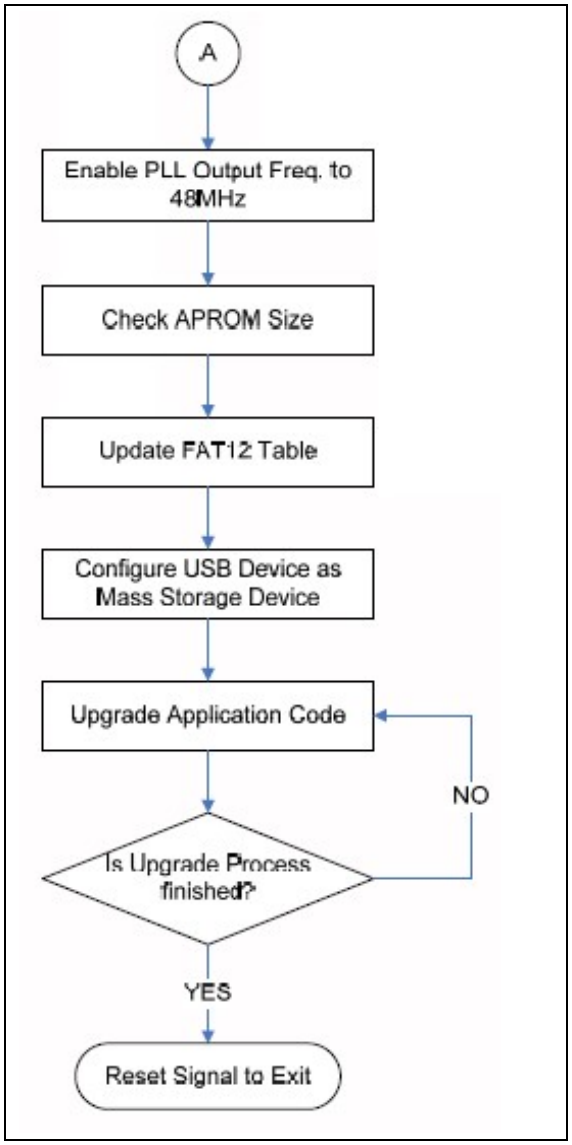


Figure 3: Mass Storage ISP Main Flow

2.4 USB MSD Endpoint Setting

This example code includes 1 interfaces that follow MSD (mass storage device) specification. Based on USB spec, endpoint 0 for control transfer is used for USB enumeration. There are 2 endpoints to upload and download Bulk report. In this example, control pipe (endpoint 0) is made of two hardware endpoints 0 and 1 in device. The interfaces and endpoints configuration are shown in Table 1.

Function	USB Composite Device	NUC126 Hardware setting
Control Transfer	Control Pipe endpoint 0: Control IN/OUT	endpoint 0: Control IN endpoint 1: Control OUT
Mass storage device	Interface 0 endpoint 1: Bulk IN endpoint 2: Bulk OUT	endpoint 2: Bulk IN endpoint 3 Bulk OUT

Table 1 Configuration of MSD interfaces and endpoints

2.5 FAT and Flash Program function

The DataFlashRead function is used to response the FAT table to PC and the DataFlashWrite() is used to program the new firmware into APROM.

```
/* This is low level read function of USB Mass Storage */
void DataFlashRead(uint32_t addr, uint32_t size, uint32_t buffer)
{
    uint32_t i;
    uint32_t *pu32;
    pu32 = (uint32_t *)buffer;

    for (i = 0; i < FLASH_PAGE_SIZE / 4; i++)
    {
        pu32[i] = 0;
    }

    if (addr == 0x00000000)
    {
        my_memcpy((uint8_t *)pu32, u8FormatData, 62);

        pu32[FLASH_PAGE_SIZE / 4 - 1] = 0xAA550000;
    }
    else
    {
        if ((addr == (FAT_SECTORS * 512)) || (addr == ((FAT_SECTORS + 1) * 512)))
        {
            pu32[0] = 0x00FFFFF8;
        }
```

```
        else if (addr == (8 * 512)) /* root dir */
        {
            my_memcpy((uint8_t *)pu32, u8RootDirData, 96);
        }
    }
}

/* This is low level write function of USB Mass Storage *
void DataFlashWrite(uint32_t addr, uint32_t size, uint32_t buffer)
{
    uint32_t *pu32;
    uint32_t i;
    pu32 = (uint32_t *)buffer;

    if ((addr >= DATA_SECTOR_ADDRESS) && (addr < (DATA_SECTOR_ADDRESS +
DATA_FLASH_STORAGE_SIZE)))
    {

        addr -= DATA_SECTOR_ADDRESS;

        if (addr == 0)
        {
            if ((pu32[0] > 0x20000000) && pu32[0] < 0x20005000)
            {
                BIN_CHECK = 1;
            }

        }

        if (BIN_CHECK == 1)
        {

            if ((addr + FLASH_OFFSET) % 2048 == 0)
            {
                FMC_Erase(addr + FLASH_OFFSET);
            }
            for (i = 0; i < FLASH_PAGE_SIZE / 4; i++)
            {
                FMC_Write((addr + FLASH_OFFSET) + (i * 4), pu32[i]);
            }
        }
    }

}
```

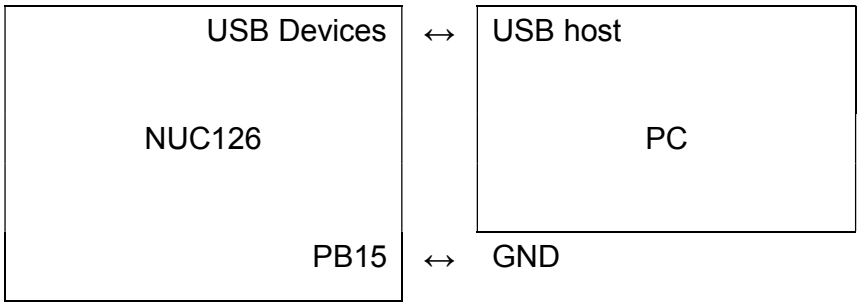
3 Software and Hardware Environment

Software Environment

- BSP version
  - NUC126 Series BSP CMSIS v3.00.003
- IDE version
  - Keil uVersion 5.26

Hardware Environment

- Circuit components
  - NuTiny-SDK-NUC126VG4AE_V1.0
  - USB mini USB cable
  - NUC126 PB15 connect to GND
- Diagram



4 Directory Information

- EC_NUC126_USBD_MSD_ISP_V1.00
  - Library — Sample code header and source files
    - CMSIS — Cortex® Microcontroller Software Interface Standard (CMSIS) by Arm® Corp.
    - Device — CMSIS compliant device header file
    - StdDriver — All peripheral driver header and source files
  - SampleCode
    - ExampleCode — Source file of example code

5 How to Execute Example Code

1. This project supports Keil uVersion 5.26 or above.
2. Browsing into sample code folder by Directory Information (section 4) and double click USBD_MSD_ISP.uvproj.
3. Enter Keil compile mode
   a. Build
   b. Download
   c. Start/Stop debug session
4. Enter debug mode
   a. Run

6 Revision History

Date	Revision	Description
Oct. 01 , 2019	1.00	1. Initially issued.

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